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#4
2838
IDS
J. Whb
F-2301

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Jeffrey M. Powell
Robert W. Baker

Serial No.: 09/872,944

Filed: 6/1/01

For: APPARATUS AND METHOD FOR
RAPID FAULT DETECTION AND
TRANSFER IN A UTILITY-
INTERACTIVE UNINTERRUPTIBLE
POWER SUPPLY

Group Art Unit: 2838

Examiner: Unknown

Atty. Dkt. No.: 11564.0040.NPUS00/ALL

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56, it is respectfully requested that this Information Disclosure Statement be entered and the documents listed on attached Form PTO-1449 be considered by the Examiner and made of record. Copies of the listed documents required by 37 C.F.R. § 1.98(a)(2) are enclosed for the convenience of the Examiner.

In accordance with 37 C.F.R §§ 1.97(g),(h), this Information Disclosure Statement is not to be construed as a representation that a search has been made, and is not to be construed to be an admission that the information cited is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

The present Information Disclosure Statement is being filed prior to the receipt of a first Official Action reflecting an examination on the merits, and hence is believed to be timely filed in accordance with 37 C.F.R § 1.97(b). No fees are believed to be due in connection with the filing of this Information Disclosure Statement; however, The Commissioner is authorized to charge any necessary fees to Deposit Account No. 01-2508/11564.0040.NPUS00/ALL. Should any additional fees under 37 C.F.R. §§ 1.16 to 1.21 be required for any reason relating to the enclosed materials, the Commissioner is authorized to deduct said fees from Deposit Account No. 01-2508/11564.0040.NPUS00/ALL

REMARKS

Reference "C3" identified in the attached Form PTO-1449 is a schematic diagram prepared by the undersigned attorney on September 27, 2001 in the course of a discussion with the inventors concerning components within a prototype UPS believed to have been released to the public more than one year than before the priority date of the instant application. The schematic depicts a utility disconnect static switch (comprising thyristors 1 & 2) with parallel forced commutation circuits (sub-circuits 3 & 4). Each of the commutation circuits includes a thyristor, inductor and capacitor.

The prototype UPS that included the referenced circuit was produced in limited quantities and was discontinued very shortly after release. All of the units were returned to the assignee of the present application and destroyed. All documentation for the project, including schematics

of the static switch commutation circuitry, have apparently been destroyed. The named inventors have undertaken a reasonable search and have not been able to identify any remaining records. The attached schematic is reproduced from their memories of the project, on which they both worked.

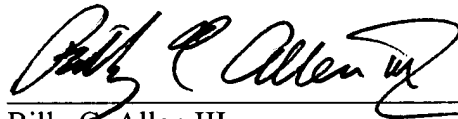
If additional information concerning this switch commutation schematic is required, Applicant and the undersigned Attorney are willing to make themselves available for interview with the Examiner.

Applicant respectfully requests that the listed documents be made of record in the present case.

Respectfully submitted,

Date: _____

10/19/01



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